

06-27-00

A

OSTROLENK, FABER, GERB & SOFFEN, LLP

Attorneys at Law

1180 Avenue of the Americas
New York, New York 10036-8403

(212) 382-0700

Telex
23 6925

Facsimile
(212) 382-0888

Cable
Ostrofaber NewYork

U.S. PTO
09/603252

06/26/00

06/26/00
U.S. PTO

Express Mail #EL334614615US

Asst. Commissioner for Patents
Washington, DC 20231

OFGS File No. : P/1071-1024
Inventor : Hideki MUTO, Koji TANAKA, Koji FURUTANI,
Takahiro WATANABE, Takanori UEJIMA & Norio NAKAJIMA
Title : HIGH FREQUENCY SWITCHING COMPONENT
Assignee : Murata Manufacturing Co., Ltd.

Enclosed herewith please find the following documents in the above-identified application for United States Letters Patent:

- 24 Pages of Specification including Abstract and Claims
- 8 Numbered Claims Calculated as 8 Claims for Fee Purposes
- 4 Sheets of Drawing Containing Figures 1 to 6. (Informal)
- X Declaration and Power of Attorney
- X Priority is Claimed under 35 U.S.C. §119:
- Convention Date 29 July 1999 for Japan Appln. S.N. 11-215012
- Convention Date 29 July 1999 for Japan Appln. S.N. 11-215013
- X Certified Priority Applications (Two)
- X Verified Statement Claiming Small Entity Status under 37 C.F.R. §1.27.
- X Assignment & Patents Recordation Cover Sheet
- X PTO-1449 & References
- X Return-Addressed Post Card

OFGS Check No. 95218, including the fee of \$730.00, calculated as follows:

Basic Filing Fee:	\$ 690.00
Additional Filing Fees:	
Total Number of Claims in Excess of 20, times \$18:	0
Number of Independent Claims in Excess of 3, times \$78:	0
One or More Multiple Dependent Claims: Total \$260:	0
Total Filing Fees or	690.00
Total Filing Fee Reduced 50% for Small Entity:	0
Assignment Recording Fee: \$40	40.00
TOTAL Filing Fee and Assignment Recording Fee:	<u>\$ 730.00</u>

In the event the actual fee is greater than the payment submitted or is inadvertently not enclosed, or if any additional fee during the prosecution of this case is not paid, the Patent and Trademark Office is authorized to charge the underpayment to Deposit Account No. 15-0700.

EXPRESS MAIL CERTIFICATE

I hereby certify that this correspondence is being deposited with the United States Postal Service as Express Mail Post Office to Addressee (mail label #EL334614615) in an envelope addressed to: Asst. Commissioner for Patents, Washington, D.C. 20231, on June 26, 2000

YVETTE CARR

Name of Person Mailing Correspondence

Signature

June 26, 2000

Date of Signature

Respectfully submitted,



James A. Finder

Registration No.: 30,173
OSTROLENK, FABER, GERB & SOFFEN, LLP
1180 Avenue of the Americas
New York, New York 10036-8403
Telephone: (212) 382-0700

HIGH FREQUENCY SWITCHING COMPONENT

BACKGROUND OF THE INVENTION

1. Field of the Invention

5 The present invention relates to high frequency switching components, and more particularly, the invention relates to high frequency switching components formed by using a multilayer circuit board so as to prevent noise.

2. Related Art

10 In digital portable phones, high frequency switches are connected to transmission circuits, reception circuits, and antennas to be used for switching connections between the transmission circuits and the antennas, and connections between the reception circuits and the antennas.

15 For example, Japanese Unexamined Patent Application Publication No. 7-202502 provides a high frequency switching component comprising such a high frequency switch, which is of keen interest to the background of the present invention.

20 The high frequency switching component is formed by using a multilayer circuit board. Circuit elements forming a high frequency switch are disposed in the multilayer circuit board and on a surface thereof, by which miniaturization of the high frequency switching component is achieved. With this arrangement, an area necessary to mount the high frequency switching component can be reduced.

In a high frequency circuit incorporating such a high frequency switch, it is essential to take measures for reducing noise, including electrostatic surges, that is, surge currents which occur when built-up static electricity is discharged.

25 In order to prevent such noise, conventionally, a separate noise-preventing component has been mounted on an appropriate wiring board with the high

frequency switching component.

However, in such an arrangement, it is necessary to allocate an area for mounting the noise-preventing component on the wiring board. As a result, this arrangement is a hindrance to the miniaturization of the wiring board. In addition, since the noise-preventing component is required to be a discrete component, this causes an increase in cost.

As another conventional noise-preventing measure, a resin coating is applied on an antenna to prevent an electrostatic current surge from entering the antenna.

However, the resin coating can come off due to deterioration of the resin.

Under such circumstances, an electrostatic surge can cause great damage to the high-frequency switch, or to an external circuit connected to the antenna or the high-frequency switch, and can lead even to the destruction of the external circuit.

As noise-preventing components, varistors and Zener diodes are known. When a varistor or a Zener diode is used as a component for preventing electrostatic surges, the capacitance between terminals must be increased. Thus, these components cannot be used in a signal line, and accordingly cannot be used for preventing electrostatic surges in a high frequency circuit incorporating such a high frequency switch.

SUMMARY OF THE INVENTION

Accordingly, it is an object of the present invention to provide a high frequency switching component capable of solving the above-described problems.

One embodiment of the present invention provides a high frequency switching component for being connected to a transmission circuit, a reception circuit, and an antenna and having two states, a state in which the transmission circuit is connected to the antenna, and a state in which the reception circuit is connected to the antenna. In addition, the high frequency switching component has a multilayer circuit board.

Linked to the multilayer circuit board, there is formed a circuit used for a high frequency switch including a transmission circuit terminal to be connected to a transmission circuit, a reception circuit terminal to be connected to the reception circuit, an antenna terminal to be connected to be the antenna, a ground terminal, a first diode whose anode is connected to the transmission circuit terminal and whose cathode is connected to the antenna terminal, a second diode whose anode is connected to the reception circuit terminal and whose cathode is connected to the ground terminal, and a signal line for connecting the transmission circuit terminal, the reception circuit terminal, and the antenna terminal via the first diode.

In addition, the transmission circuit terminal, the reception circuit terminal, the antenna terminal, the ground terminal, the first diode, and the second diode are disposed on a surface of the multilayer circuit board. Furthermore, at least a part of the single line is disposed inside the multilayer circuit board.

In such a high frequency switching component, in order to solve the above-described technological problems, the present invention provides a structure in which an inductor is disposed between the signal line and the ground terminal to eliminate noise within or on the surface of the multilayer circuit board.

The above inductor is preferably provided by a line electrode disposed inside the multilayer circuit board.

In addition, in this embodiment of the present invention, particularly, it is preferable that the inductor is disposed between the ground terminal and a part at which the signal line is connected to the antenna terminal.

Further, in such a high frequency switching component, another embodiment of the present invention provides a structure in which an LC filter is disposed on the signal line to eliminate noise, and the LC filter is arranged inside or on the surface of the multilayer circuit board.

The above LC filter is preferably disposed inside the multilayer circuit board.

In addition, in this embodiment of the present invention, particularly, it is

preferable that the LC filter is disposed at a part at which the signal line is connected to the antenna terminal.

Other features and advantages of the present invention will become apparent from the following description of the invention which refers to the accompanying drawings.

BRIEF DESCRIPTION OF THE DRAWINGS

Fig. 1 is the circuit diagram of a circuit structure arranged in a high frequency switching component 1 according to a first embodiment of the present invention.

Fig. 2 is a perspective view showing the appearance of the high frequency switching component 1 having the circuit structure shown in Fig. 1.

Fig. 3 is an exploded perspective view of the multilayer circuit board 2 shown in Fig. 2.

Fig. 4 is a circuit diagram showing a circuit structure arranged in a high frequency switching component 101 according to a second embodiment of the present invention.

Fig. 5 is a perspective view showing the appearance of the high frequency switching component 101 having the circuit structure shown in Fig. 4.

Fig. 6 is an exploded perspective view of a multilayer circuit board 102 shown in Fig. 5.

DETAILED DESCRIPTION OF EMBODIMENTS OF THE INVENTION

Figs. 1 and 3 illustrate a high frequency switching component 1 according to a first embodiment of the present invention.

First, referring to Fig. 1, a description will be given of the structure of a circuit disposed in the high frequency switching component 1.

The high frequency switching component 1 includes a transmission circuit terminal Tx for being connected to a transmission circuit, a reception circuit terminal

Rx for being connected to a reception circuit, an antenna terminal ANT for being connected to an antenna, and a ground terminal GND. In addition, the high frequency switching component 1 includes a first control terminal T1 and a second control terminal T2.

5 The transmission circuit terminal Tx is connected to the anode of a first diode D1 via a first capacitor C1. The cathode of the first diode D1 is connected to the antenna terminal ANT via a second capacitor C2. The anode of the first diode D1 is connected to a ground terminal GND, that is, the anode is grounded, via a series circuit constituted of a first transmission line SL1 and a third capacitor C3. In
10 addition, the first control terminal T1 is connected to an intermediate point between the first transmission line SL1 and the third capacitor C3, and a first control circuit for switching the high frequency switching component 1 is to be connected to the first control terminal T1.

 The second capacitor C2 connected to the antenna terminal ANT is connected
15 to the reception circuit terminal Rx via a series circuit constituted of a second transmission line SL2 and a fourth capacitor C4. In addition, the anode of a second diode D2 is connected to the intermediate point between the second transmission line SL2 and the fourth capacitor C4. The cathode of the second diode D2 is connected to a ground terminal GND via a fifth capacitor C5, that is, the cathode thereof is
20 grounded. The second control terminal T2 is connected to the intermediate point between the second diode D2 and the fifth capacitor C5 via a resistor R. A second control circuit for switching the high frequency switching component 1 is to be connected to the second control terminal T2.

 In such a high frequency switching component 1, in order to connect the
25 transmission circuit and the antenna in order to perform transmission, a positive voltage is applied to the first control terminal T1 by the first control circuit, which is not shown in the figure. With this applied voltage, both the first and second diodes D1 and D2 are switched on. In this situation, the DC voltage applied to the first

control terminal T1 is applied only to a circuit including the first and second diodes D1 and D2, and the voltage is blocked by the first to fifth capacitors C1 to C5.

As described above, when the first and second diodes D1 and D2 are switched on, a signal applied to the transmission circuit terminal Tx by the
5 transmission circuit is transmitted to the antenna via the antenna terminal ANT. In addition, the signal from the transmission circuit is not transmitted to the reception circuit terminal Rx, since the second transmission line SL2 is placed in a resonant condition by being grounded via the second diode D2, and as a result, an extremely large impedance is observed in the direction of the reception circuit terminal Rx from
10 a node A.

On the other hand, in the high frequency switching component 1, in order to connect the reception circuit and the antenna to perform reception, no voltage is applied to the first control terminal T1, and a positive voltage is applied to the second control terminal T2. With this applied voltage, both the first and second diodes D1
15 and D2 are switched off. As a result, a signal received from the antenna via the antenna terminal ANT is transmitted to the reception circuit via the reception circuit terminal Rx, and is not transmitted to the transmission circuit via the transmission circuit terminal Tx.

In this way, with the high frequency switching component 1, control voltages
20 are applied to the first and second control terminals T1 and T2 so as to switch to a state in which the transmission circuit is connected to the antenna and a state in which the reception circuit is connected to the antenna.

In this embodiment, the high frequency switching component 1 described above also includes the first to third inductors L1 to L3. The inductors L1 to L3 are
25 disposed respectively between the ground terminal GND and the antenna terminal ANT, the transmission circuit terminal Tx, and the reception circuit terminal Rx. With this arrangement, the inductors L1 to L3 serve to eliminate noise such as electrostatic surges occurring on the signal line 3.

More specifically, the first inductor L1 is disposed between the part at which the signal line 3 is connected to the antenna terminal ANT and the ground terminal GND. The second inductor L2 is disposed between the part at which the signal line 3 is connected to the transmission circuit terminal Tx and the ground terminal GND.

5 The third inductor L3 is disposed between the part at which the signal line 3 is connected to the reception circuit terminal Rx and the ground terminal GND.

With the first to third inductors L1 to L3, when a noise component is generated in a signal running through the signal line 3, the noise component flows through one of the inductors L1 to L3 to the ground terminal GND, with the result
10 that the noise component can be eliminated. Particularly, the first inductor L1 disposed near the antenna terminal ANT effectively allows an electrostatic surge that enters from the antenna to flow to the ground terminal GND, and serves to prevent the electrostatic surge from entering either the high frequency switching component
15 1 or an external circuit connected to the switching component. As a result, the first inductor L1 prevents damage both to the high frequency switching component 1 and to external circuits.

Furthermore, by appropriately selecting the inductance value of each of the first to third inductors L1 to L3, the inductors L1 to L3 are adapted to be suitable for prevention of electrostatic surges, and for the prevention of noise on the low-
20 frequency side, between direct circuit and a signal frequency. In addition, the inductors L1 to L3 can have the additional function of performing matching adjustments in the high frequency switching component 1.

Fig. 2 is a perspective view illustrating the appearance of the high frequency switching component 1 comprising the circuit shown in Fig. 1. Fig. 3 is an exploded
25 perspective view of a multilayer circuit board 2 disposed in the high frequency switching component 1 shown in Fig. 2. In Figs. 2 and 3, the same reference numerals are given to parts equivalent to those shown in Fig. 1 so that the linkages between Figs. 1, 2, and 3 can be easily understood.

The multilayer circuit board 2 disposed in the high frequency switching component 1, as shown in Fig. 3, is formed by laminating a plurality of insulation layers 4 to 10. The insulation layers 4 to 10 are formed of a dielectric material.

Referring to Fig. 3, the description of the circuit board 2 will start from the insulation layer 4 at the bottom. Over almost the entire first insulation layer 4, a ground electrode 11 is formed. The ground electrode 11 is connected to the ground terminal GND shown in Fig. 2.

On a second insulation layer 5, first to third inductors L1 to L3 are formed by line electrodes. These line electrodes for forming the first to third inductors L1 to L3 provide microstrip line structures with the ground electrode 11.

An end of each of the line electrodes forming the first to third inductors L1 to L3 is connected to the ground electrode 11 via a respective one of via-holes 12, 13, and 14. The other end of the line electrode forming the first inductor L1 is connected to the antenna terminal ANT shown in Fig. 2. The other end of the line electrode forming the second inductor L2 is connected to the transmission circuit terminal Tx shown in Fig. 2. The other end of the line electrode forming the third inductor L3 is connected to the reception circuit terminal Rx shown in Fig. 2.

On a third insulation layer 6, a capacitor electrode 15 used for a third capacitor C3 is formed. The other capacitor electrode used for the third capacitor C3 is provided by the ground electrode 11.

On a fourth insulation layer 7, line electrodes used for first and second transmission lines SL1 and SL2 are formed. These first and second transmission lines SL1 and SL2 comprise microstrip line structures formed by these line electrodes and the ground electrode 11. An end of the first transmission line SL1 is connected to a capacitor electrode 15 used for the third capacitor C3 through a via-hole 16.

On a fifth insulation layer 8, capacitor electrodes 17, 18, and 19 used for the first, second, and fourth capacitors C1, C2, and C4 are formed. The capacitor

electrode 17 used for the first capacitor C1 is connected to the second inductor L2 by a via-hole 20. The capacitor electrode 18 used for the second capacitor C2 is connected to the first inductor L1 by a via-hole 21. The capacitor electrode 19 used for the fourth capacitor C4 is connected to the third inductor L3 by a via-hole 22.

5 On a sixth insulation layer 9, the other capacitor electrodes 23, 24, and 25 used for the first, second, and fourth capacitors C1, C2, and C4 are formed. These capacitor electrodes 23, 24, and 25 oppose the capacitor electrodes 17, 18, and 19 through the insulation layer 9.

10 The capacitor electrode 23 used for the first capacitor C1 is connected to the first transmission line SL1 by a via-hole 26. The capacitor electrode 24 used for the second capacitor C2 is connected to one end of the second transmission line SL2 by a via-hole 27. The capacitor electrode 25 used for the fourth capacitor C4 is connected to the other end of the second transmission line SL2 by a via-hole 28.

15 On a seventh insulation layer 10 at the top are formed conductive lands 29, 30, 31, 32, 33, 34, and 35.

 The conductive land 29 is connected to the first control terminal T1 shown in Fig. 2, and is also connected to the first transmission line SL1 and the capacitor electrode 15 used for the third capacitor C3 through the via-hole 16.

20 The conductive land 30 is connected to the second control terminal T2 shown in Fig. 2.

 The conductive land 31 is connected to the capacitor electrode 24 used for the second capacitor C2 and the second transmission line SL2 through the above via-hole 27.

25 The conductive land 32 is connected to the ground terminal GND shown in Fig. 2.

 The conductive land 33 is connected to the capacitor electrode 23 used for the first capacitor C1 and the first transmission line SL1 by the above via-hole 26.

 The conductive land 35 is connected to the capacitor electrode 25 used for the

fourth capacitor C4 and the second transmission line SL2.

The fifth capacitor C5, the first and second diodes D1 and D2, and the resistor R shown in Fig. 1 are composed of chips. As shown in both Figs. 2 and 3, the fifth capacitor C5 is mounted in such a manner that the conductive land 32 is linked to the conductive land 34. In addition, the first diode D1 is mounted in such a manner that the conductive land 31 is linked to the conductive land 33. The second diode D2 is mounted in such a manner that the conductive land 34 is linked to the conductive land 35. The resistor R is mounted such that the conductive land 30 is linked to the conductive land 34.

As shown above, the high frequency switching component 1 has the circuit structure shown in Fig. 1. In the high frequency switching component 1, as clearly shown in Fig. 2, on the surface of the multilayer circuit board 2 are formed the transmission circuit terminal Tx, the reception circuit terminal Rx, the antenna terminal ANT, the ground terminal GND, the first diode D1, the second diode D2, the fifth capacitor C5, and the resistor R. In addition, at least a part of the signal line 3 connecting the transmission circuit terminal Tx and the reception circuit terminal Rx to the antenna terminal ANT is disposed inside the multilayer circuit board 2. In addition, the first to third inductors L1 to L3 are provided by the line electrodes disposed inside the multilayer circuit board 2.

As described above, the present invention has been described by referring to the disclosed embodiment. However, other various modifications are applicable without departing from the scope of the invention.

For example, although the above embodiment has provided the first to third inductors L1 to L3 which are formed by the line electrodes disposed in the multilayer circuit board 2, at least one of the first to third inductors L1 to L3 may instead be formed as a chip to be mounted on the surface of the multilayer circuit board 2. Also, chip capacitors can be substituted for the first to fourth capacitors C1 to C4 disposed inside the multilayer circuit board 2. On the other hand, the fifth capacitor

C5 and/or the resistor R which are each formed as a chip may instead be disposed inside the multilayer circuit board 2.

Furthermore, in the above embodiment shown in the figures, each of the first to third inductors L1 to L3 is arranged corresponding to a respective one of the antenna terminal ANT, the transmission circuit terminal Tx, and the reception circuit terminal Rx. However, one or more of the three inductors L1 to L3 may be omitted.

As described above, according to the above described structure and arrangement, since inductors used for eliminating noise are disposed between the signal line and the ground terminal in the high frequency switching component, noise components contained in high frequency signals on the signal line can be carried to the ground terminal via the inductors. As a result, noise such as electrostatic surges can be reduced, and, for example, damage caused by the electrostatic surges to the high frequency circuit can effectively be reduced.

Furthermore, since such inductors used for eliminating noise are arranged either on the surface of the multilayer circuit board or inside the multilayer circuit board included in the high frequency switching component, the mounting of these inductors does not increase the area necessary to mount the high frequency switching component. Furthermore, when compared with a case in which an additional noise-preventing component is used, it is possible to decrease the area occupied on a wiring board by a high frequency circuit including the high frequency switching component, and production cost can also be reduced.

In the present invention, miniaturization of the high frequency switching component can be achieved by using the line electrodes as the inductors for eliminating noise on lines disposed in the multilayer circuit board.

Furthermore, in the invention, since the inductor is disposed between the ground terminal and the part at which the signal line is connected to the antenna terminal, the inductor effectively works against electrostatic surges entering from an antenna. Therefore, damage caused by electrostatic surges to the high frequency

switch and other circuits can be reduced.

Figs. 4 to 6 illustrate a high frequency switching component 101 according to a second embodiment of the present invention.

First, referring to Fig. 4, a description will be given of the structure of a
5 circuit disposed in the high frequency switching component 101.

The high frequency switching component 101 includes a transmission circuit
terminal Tx0 for being connected to a transmission circuit, a reception circuit
terminal Rx0 for being connected to a reception circuit, an antenna terminal ANT0
for being connected to an antenna, and a ground terminal GND0. In addition, the
10 high frequency switching component 101 includes a first control terminal T10 and a
second control terminal T20.

The transmission circuit terminal Tx0 is connected to the anode of a first
diode D10 via a first capacitor C10 and a second capacitor C20 connected in series.
The cathode of the first diode D10 is connected to the antenna terminal ANT0 via a
15 third capacitor C30 and a fourth capacitor C40 connected in series.

The anode of the first diode D10 is connected to a ground terminal GND0,
that is, the anode is grounded, via a series circuit constituted of a first transmission
line SL10 and a fifth capacitor C50. In addition, the first control terminal T10 is
connected to the intermediate point between the first transmission line SL10 and the
20 fifth capacitor C50, and a control circuit connected to the first control terminal T10
can be used to control switching performed by the high frequency switching
component 101.

The reception circuit terminal Rx0 is connected to the third capacitor C30 and
the fourth capacitor C40 connected to the antenna terminal ANT0 via a series circuit
25 constituted of a second transmission line SL20 and the sixth and seventh capacitors
C60 and C70.

In addition, the anode of a second diode D20 is connected to the intermediate
point between the second transmission line SL20 and the sixth capacitor C60. The

cathode of the second diode D20 is connected to a ground terminal GND0 via an eighth capacitor C80, that is, the cathode thereof is grounded. The second control terminal T20 is connected to the intermediate point between the second diode D20 and the eighth capacitor C80 via a resistor R0. Another control circuit, connected to
5 the second control terminal T20, can be used to control switching performed by the high frequency switching component 101.

In such a high frequency switching component 101, for connecting the transmission circuit and the antenna to perform transmission, a positive voltage is applied to the first control terminal T10 from the control circuit, which is not shown
10 in the figure. With this applied voltage, both the first and second diodes D10 and D20 are switched on. In this situation, since the DC voltage is blocked by the first to eighth capacitors C10 to C80, the voltage applied to the first control terminal T10 is applied only to a circuit including the first and second diodes D10 and D20.

As described above, when the first and second diodes D10 and D20 are
15 switched on, a signal applied to the transmission circuit terminal Tx by the transmission circuit is transmitted to the antenna via the antenna terminal ANT0. In addition, the signal from the transmission circuit is not transmitted to the reception circuit terminal Rx0, since the second transmission line SL20 is placed in a resonant state by being grounded via the second diode D20, and as a result, an impedance
20 observed in the direction of the reception circuit terminal Rx0 from a connecting point A0 is extremely high.

Meanwhile, in the high frequency switching component 101, in order to connect the reception circuit and the antenna to perform reception, no voltage is applied to the first control terminal T10, and a positive voltage is applied to the
25 second control terminal T20. With this applied voltage, both the first and second diodes D10 and D20 are switched off. As a result, a signal received from the antenna via the antenna terminal ANT0 is transmitted to the reception circuit via the reception circuit terminal Rx0. Further, the received signal is not transmitted to the

transmission circuit via the transmission circuit terminal Tx0.

In this way, with the use of the high frequency switching component 101, the voltage applied to each of the first and second control terminals T10 and T20 is controlled so as to switch to either a state in which the transmission circuit is
5 connected to the antenna, or a state in which the reception circuit is connected to the antenna.

In the high frequency switching component 101 described above, according to this embodiment, the first to sixth inductors L10 to L60 are disposed in such a manner that the LC filters LC10, LC20, and LC30 are formed by using both the
10 inductors L10 to L60 and the aforementioned first, second, third, fourth, sixth, and seventh capacitors C10, C20, C30, C40, C60, and C70 together. The LC filters LC10, LC20, and LC30 each form a fourth-order high pass filter.

The first LC filter LC10 is formed by the first and second capacitors C10 and C20, and the first and second inductors L10 and L20, which are disposed between
15 the signal line 103 passing through the capacitors C10 and C20 and the ground terminal GND0.

The second LC filter LC20 is formed by the third and fourth capacitors C30 and C40, and the third and fourth inductors L30 and L40, which are disposed between the signal line 103 passing through the capacitors C30 and C40 and the
20 ground terminal GND0.

The third LC filter LC30 is formed by the sixth and seventh capacitors C60 and C70, and the fifth and sixth inductors L50 and L60, which are disposed between the signal line 103 passing through the capacitors C60 and C70 and the ground
terminal GND0.

As described above, since these LC filters LC10 to LC30 form high pass
25 filters, these LC filters effectively serve to eliminate noise components of frequencies lower than a signal frequency on the signal line 103, such as electrostatic surges.

Particularly, the second LC filter LC20 disposed near the antenna terminal ANT0 effectively allows an electrostatic surge which enters from the antenna to be attenuated, and serves to prevent the electrostatic surge from entering either into the high frequency switching component 101 or into external circuits attached to the switching component. As a result, the second LC filter LC20 prevents damage both

Furthermore, the LC filters LC10 to LC30 can be adapted to have the additional function of performing matching adjustments between the transmission circuit terminal Tx0, the reception circuit terminal Rx0, and the antenna terminal ANT0 in the high frequency switching component 101.

Fig. 5 is a perspective view illustrating the appearance of the high frequency switching component 101 comprising the circuit shown in Fig. 4. Fig. 6 is an exploded perspective view of a multilayer circuit board 102 disposed in the high frequency switching component 101 shown in Fig. 5. In Figs. 5 and 6, the same reference numerals are given to parts equivalent to those shown in Fig. 4 so that the linkages between Figs. 4, 5 and 6 can be easily understood.

The multilayer circuit board 102 disposed in the high frequency switching component 101, as shown in Fig. 6, is formed by laminating a plurality of insulation layers 104 to 113. The insulation layers 104 to 113 are, for example, formed of dielectric materials.

Referring to Fig. 6, the description will start from the insulation layer 104 at the bottom. On the first insulation layer 104, capacitor electrodes 114, 115, and 116 for first, fourth, and seventh capacitors C10, C40, and C70 are formed. The capacitor electrode 114 for the first capacitor C10 is connected to the transmission circuit terminal Tx0 shown in Fig. 5. The capacitor electrode 115 for the fourth capacitor C40 is connected to the antenna terminal ANT0 shown in Fig. 5. The capacitor electrode 116 for the seventh capacitor C70 is connected to the reception circuit terminal Rx0 shown in Fig. 5.

On a second insulation layer 105, other capacitor electrodes 117, 118, and 119 for the first, fourth, and seventh capacitors C10, C40, and C70 are formed. These capacitor electrodes 117, 118, and 119 oppose the capacitor electrodes 114, 115, and 116 described above through the insulation layer 105.

5 On a third insulation layer 106, first, fourth, and sixth inductors L10, L40, and L60 are formed by line electrodes. The line electrodes for forming the first, fourth, and sixth inductors L10, L40, and L60 provide microstrip line structures with a ground electrode 120, which will be described below. In addition, the fourth and sixth inductors L40 and L60 are provided by a series of the line electrodes.

10 On a fourth insulation layer 107 are formed capacitor electrodes 121, 122, and 123 for second, third, and sixth capacitors C20, C30, and C60.

On a fifth insulation layer 108 are formed other capacitor electrodes 124, 125, and 126 for the second, third, and sixth capacitors C20, C30, and C60. These capacitor electrodes 124, 125, and 126 oppose the aforementioned capacitor electrodes 121, 122, and 123 via the insulation layer 108.

15 On a sixth insulation layer 109, second, third, and fifth inductors L20, L30, and L50 are formed by line electrodes. These line electrodes for forming the second, third, and fifth inductors L20, L30, and L50 provide microstrip line structures with the ground electrode 120, which will be described below. In addition, the third and fifth inductors L30 and L50 are provided by a series of the line electrodes.

20 On almost the entire seventh insulation layer 110, the ground electrode 120 is formed. The ground electrode 120 is connected to the ground terminal GND0 shown in Fig. 5.

25 On an eighth insulation layer 111 is formed a capacitor electrode 127 for the fifth capacitor C50. The other capacitor electrode for the fifth capacitor C50 is provided by the aforementioned ground electrode 120.

On a ninth insulation layer 112, line electrodes for first and second transmission lines SL10 and SL20 are formed. These first and second transmission

lines SL10 and SL20 comprise microstrip line structures formed by their line electrodes and the ground electrode 120.

On a tenth insulation layer 112 at the top are formed conductive lands 128, 129, 130, 131, 132, 133, and 134. The conductive land 128 is connected to the first control terminal T10 shown in Fig. 5. The conductive land 129 is connected to the second control terminal T20 shown in Fig. 5. The conductive land 131 is connected to the ground terminal GND0 shown in Fig. 5.

In addition, a description will be given of the connection between the insulation layers 104 to 113. The conductive land 128, the first transmission line SL10, and the capacitor electrode 127 for the fifth capacitor C50 are connected to one another through a via-hole 135.

The conductive land 130, the second transmission line SL20, the inductor L30, and the capacitor electrode 125 for the third capacitor C30 are connected to one another through a via-hole 136.

The conductive land 132, the first transmission line SL10, the second inductor L20, and the capacitor 124 for the second capacitor C20 are connected to one another through a via-hole 137.

The conductive land 134, the second transmission line SL20, the fifth inductor L50, and the capacitor electrode 126 for the sixth capacitor C60 are connected to one another through a via-hole 138.

The first inductor L10 and the second inductor L20 are connected to the ground electrode 120 through a via-hole 139. The third, fourth, fifth, and sixth inductors L30, L40, L50, and L60 are connected to the ground electrode 120 through a via-hole 140.

The capacitor electrode 121 for the second capacitor C20, the first inductor L10, and the capacitor electrode 117 for the first capacitor C10 are connected to one another through a via-hole 141.

The capacitor electrode 122 for the third capacitor C30, the fourth inductor

L40, and the capacitor electrode 118 for the fourth capacitor C40 are connected to one another through a via-hole 142.

The capacitor electrode 123 for the sixth capacitor C60, the sixth inductor L60, and the capacitor electrode 119 for the seventh capacitor C70 are connected to one another through a via-hole 143.

The eighth capacitor C80, the first and second diodes D10 and D20, and the resistor R0 shown in Fig. 4 are formed of chips. As seen in Figs. 5 and 6, the eighth capacitor C80 is mounted such that the conductive land 131 is linked to the conductive land 133. In addition, the first diode D10 is mounted such that the conductive land 130 is linked to the conductive land 132. The second diode D20 is mounted such that the conductive land 133 is linked to the conductive land 134. The resistor R0 is mounted such that the conductive land 129 is linked to the conductive land 133.

In this way, the high frequency switching component 101 provides a circuit structure shown in Fig. 4. In the high frequency switching component 101, as clearly shown in Fig. 5, the transmission circuit terminal Tx0, the reception circuit terminal Rx0, the antenna terminal ANT0, the ground terminal GND0, the first diode D10, the second diode D20, the eighth capacitor C80, and the resistor R0 are disposed on the surface of the multilayer circuit board 102. In addition, at least a part of the signal line 103 for connecting the transmission circuit terminal Tx0 and the reception circuit terminal Rx0 to the antenna terminal ANT0 is disposed inside the multilayer circuit board 102. Furthermore, the first to third LC filters LC10 to LC30 provided by the inductors L10 to L60 and the capacitors C10 to C40, C60 and C70, respectively, are disposed inside the multilayer circuit board 102.

Although the invention has been described by referring to the disclosed embodiment, various modifications can be applied within the scope of the present invention.

For example, in the embodiment shown in the figures, the first to third LC

filters LC10 to LC30 are provided by the inductors L10 to L60, and the capacitors C10 to C40, C60 and C70. However, at least one of the LC filters LC10 to LC30 may be formed by a chip filter component. Alternatively, at least one of the inductors and the capacitors forming at least one of the LC filters LC10 to LC30 may be formed as a chip to be mounted on the surface of the multilayer circuit board 102. The same is true of the fifth capacitor C50 disposed inside the multilayer circuit board 102. On the other hand, the resistor R0 formed as a chip component may instead be disposed inside the multilayer circuit board 102.

Furthermore, in the embodiment shown in the figures, the first to third LC filters LC10 to LC30 are connected respectively to the antenna terminal ANT0, the transmission circuit terminal Tx0, and the reception circuit terminal Rx0. However, one or more of the three LC filters LC10 to LC30 may be omitted.

In addition, in the embodiment shown in the figures, the LC filters LC10 to LC30 form fourth-order high pass filters. However, regardless of what order filter they are, according to the frequency of the noise to be eliminated, low pass filters or band pass filters may be used instead.

As described above, according to the above described structure and arrangement, since LC filters for eliminating noise are connected to the signal line in the high frequency switching component, a noise component contained in a high frequency signal on the signal line can be eliminated via the LC filters. As a result, for example, noise such as an electrostatic surge can be reduced, and damage caused by the electrostatic surge to the high frequency circuit can be effectively reduced. In order to enhance such a noise-eliminating effect, it is only necessary to increase the number of stages in the LC filter.

In addition, since the noise-eliminating LC filters are disposed inside or on the surface of the multilayer circuit board arranged in the high frequency switching component, they do not increase the area required to mount the high frequency switching component. Furthermore, when compared with a case in which a separate

noise-preventing component is added, the area of the wiring board required for mounting a high frequency circuit including the high frequency switching component can be made smaller, and the cost of production can also be reduced.

5 In addition, when the inductor is inserted between the signal line and the ground to prevent noise, the smaller the inductance value, the greater the noise-eliminating effect. However, in contrast, since the insertion loss becomes greater, negative effects are caused on the characteristics of the high frequency circuit. However, as in the case of the present invention, with the use of the LC filters, even though the inductance value is smaller, it is easy to obtain matching. As a result,
10 with no problem to decrease the inductance value, the noise-eliminating effects can be enhanced.

Furthermore, since the LC filters also can function as matching adjustment circuits, matching adjustment circuits are unnecessary or can be simplified. With this arrangement, reduction of the mounting area and the cost can be achieved.

15 In this invention, since the LC filters for eliminating a noise are disposed inside the multilayer circuit board, the high frequency switching component can be miniaturized.

Additionally, in the present invention, since the LC filter is inserted at the part at which the signal line is connected to the antenna terminal, the LC filter
20 effectively serves against an electrostatic surge entering from the antenna. As a result, damage caused by such an electrostatic surge to the high frequency switch can be reduced, and damage to external circuits attached thereto.

While the invention has been particularly shown and described with reference to preferred embodiments thereof, it will be understood by those skilled in the art
25 that the foregoing and other changes in form and details may be made therein without departing from the spirit of the invention.

WHAT IS CLAIMED IS:

1. A high frequency switching component for being connected to a transmission circuit, a reception circuit, and an antenna to be used for switching either to a state in which the transmission circuit is connected to the antenna, or a state in which the reception circuit is connected to the antenna, comprising:
 - a multilayer circuit board, on which there is formed a circuit including:
 - a transmission circuit terminal to be connected to the transmission circuit;
 - a reception circuit terminal to be connected to the reception circuit;
 - an antenna terminal to be connected to the antenna;
 - a ground terminal;
 - a first diode whose anode is connected to the transmission circuit terminal and the cathode thereof is connected to the antenna terminal;
 - a second diode whose anode is connected to the reception circuit terminal and the cathode thereof is connected to the ground terminal;
 - a signal line for connecting the transmission circuit terminal, the reception circuit terminal, and the antenna terminal via the first diode; and
 - an inductor disposed between the signal line and the ground terminal to reduce noise on the signal line;
 - in which the transmission circuit terminal, the reception circuit terminal, the antenna terminal, the ground terminal, the first diode, and the second diode are disposed on a surface of the multilayer circuit board; and
 - at least a part of the signal line is disposed inside the multilayer circuit board.
2. The high frequency switching component according to Claim 1, wherein the inductor is provided by a line electrode disposed inside the multilayer circuit board.

3. The high frequency switching component according to Claim 2, wherein the inductor is disposed between the ground terminal and a part of the signal line at which the signal line is connected to the antenna terminal.

4. The high frequency switching component according to Claim 1, wherein the inductor is disposed between the ground terminal and a part of the signal line at which the signal line is connected to the antenna terminal.

5. A high frequency switching component for being connected to a transmission circuit, a reception circuit, and an antenna to be used for switching to either a state in which the transmission circuit is connected to the antenna, or a state in which the reception circuit is connected to the antenna, comprising:

- 5 a multilayer circuit board, on which there is formed a circuit including:
 a transmission circuit terminal to be connected to the transmission circuit;
 a reception circuit terminal to be connected to the reception circuit;
 an antenna terminal to be connected to be the antenna;
 a ground terminal;
- 10 a first diode whose anode is connected to the transmission circuit terminal
and the cathode thereof is connected to the antenna terminal;
 a second diode whose anode is connected to the reception circuit terminal and
the cathode thereof is connected to the ground terminal;
- a signal line for connecting the transmission circuit terminal, the reception
15 circuit terminal, and the antenna terminal via the first diode; and
 an LC filter connected to the signal line to reduce noise on the signal line;
 in which the transmission circuit terminal, the reception circuit terminal, the
antenna terminal, the ground terminal, the first diode, and the second diode are
disposed on a surface of the multilayer circuit board; and

20 at least a part of the signal line being disposed inside the multilayer circuit board.

6. The high frequency switching component according to Claim 5, wherein the LC filter is disposed inside the multilayer circuit board.

25 7. The high frequency switching component according to Claim 6, wherein the LC filter is connected to a part of the signal line at which the signal line is connected to the antenna terminal.

8. The high frequency switching component according to Claim 5, wherein the LC filter is connected to a part of the signal line at which the signal line is connected to the antenna terminal.

HIGH FREQUENCY SWITCHING COMPONENT

ABSTRACT OF THE DISCLOSURE

A high frequency switching component for being connected to a transmission circuit, a reception circuit, and an antenna to be used for switching to either a state in which the transmission circuit is connected to the antenna, or a state in which the reception circuit is connected to the antenna, comprising: a multilayer circuit board, on which there is formed a circuit including: a transmission circuit terminal to be connected to the transmission circuit; a reception circuit terminal to be connected to the reception circuit; an antenna terminal to be connected to the antenna; a ground terminal; a first diode whose anode is connected to the transmission circuit terminal and the cathode thereof is connected to the antenna terminal; a second diode whose anode is connected to the reception circuit terminal and the cathode thereof is connected to the ground terminal; a signal line for connecting the transmission circuit terminal, the reception circuit terminal, and the antenna terminal via the first diode; and an inductor or an LC filter disposed between the signal line and the ground terminal to reduce noise on the signal line; in which the transmission circuit terminal, the reception circuit terminal, the antenna terminal, the ground terminal, the first diode, and the second diode are disposed on a surface of the multilayer circuit board; at least a part of the signal line being disposed inside the multilayer circuit board; and the inductor being disposed either inside or on the surface of the multilayer circuit board.

Fig. 1

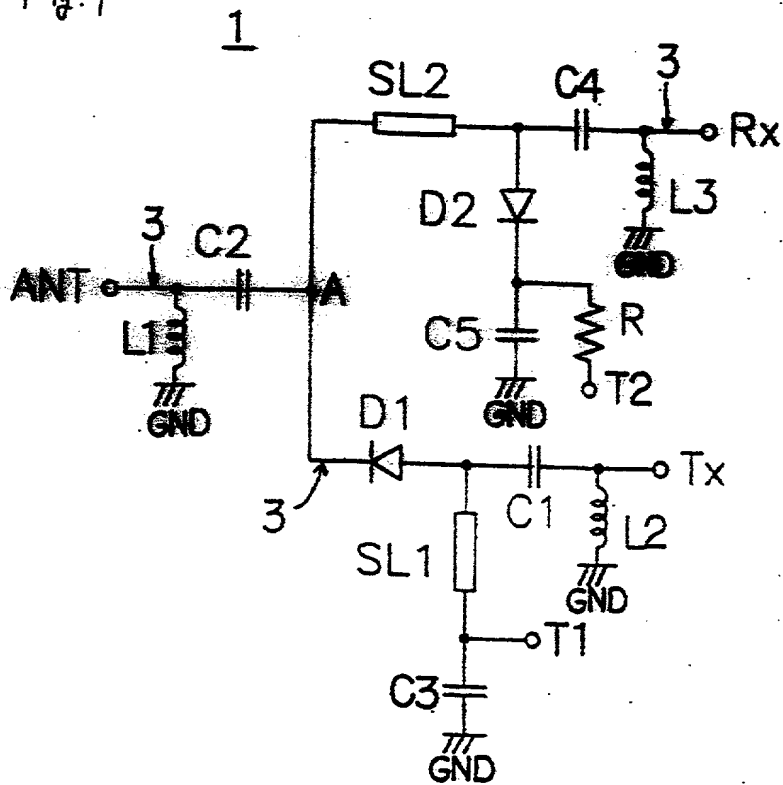


Fig. 2

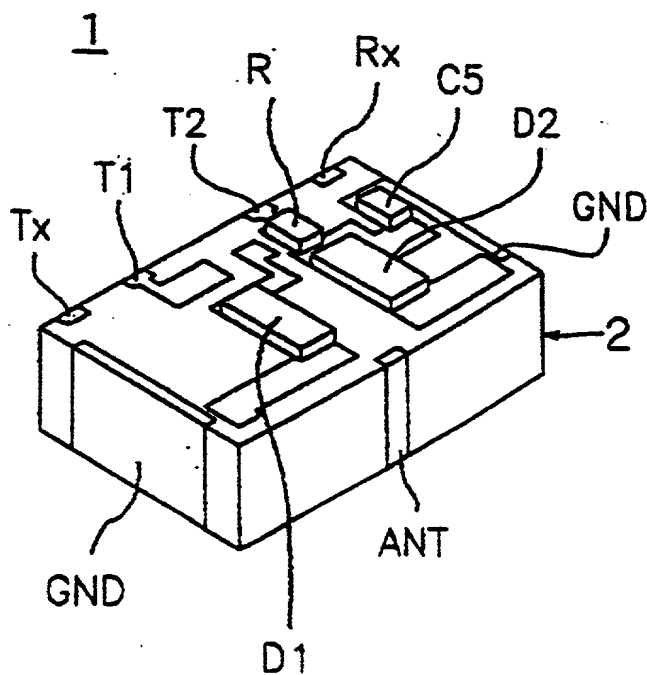


Fig. 3

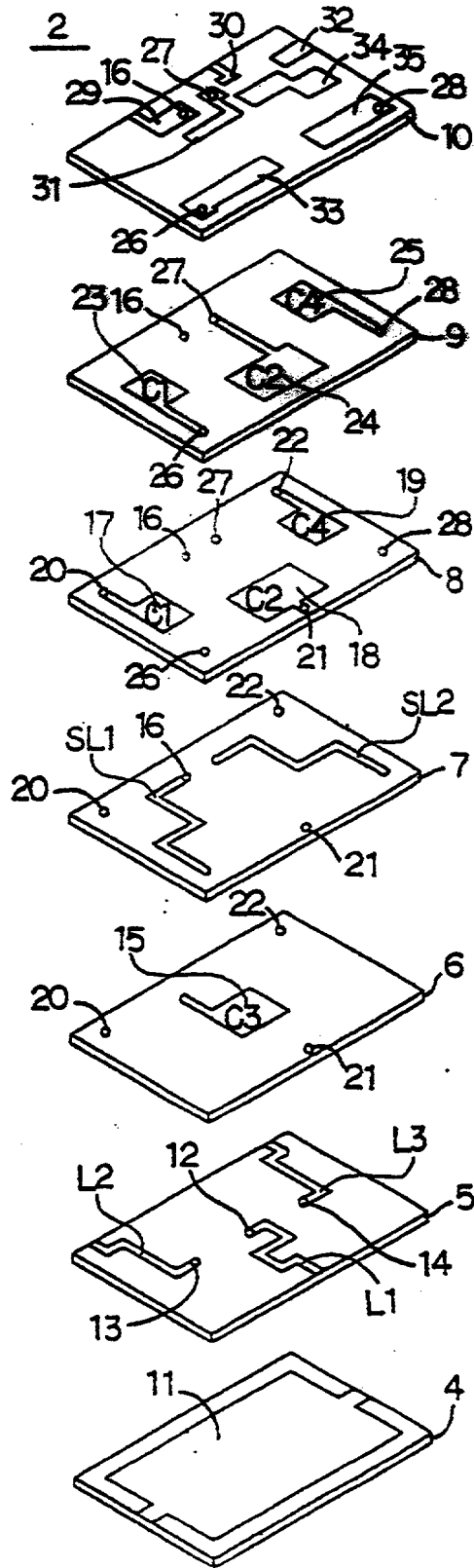


Fig. 4

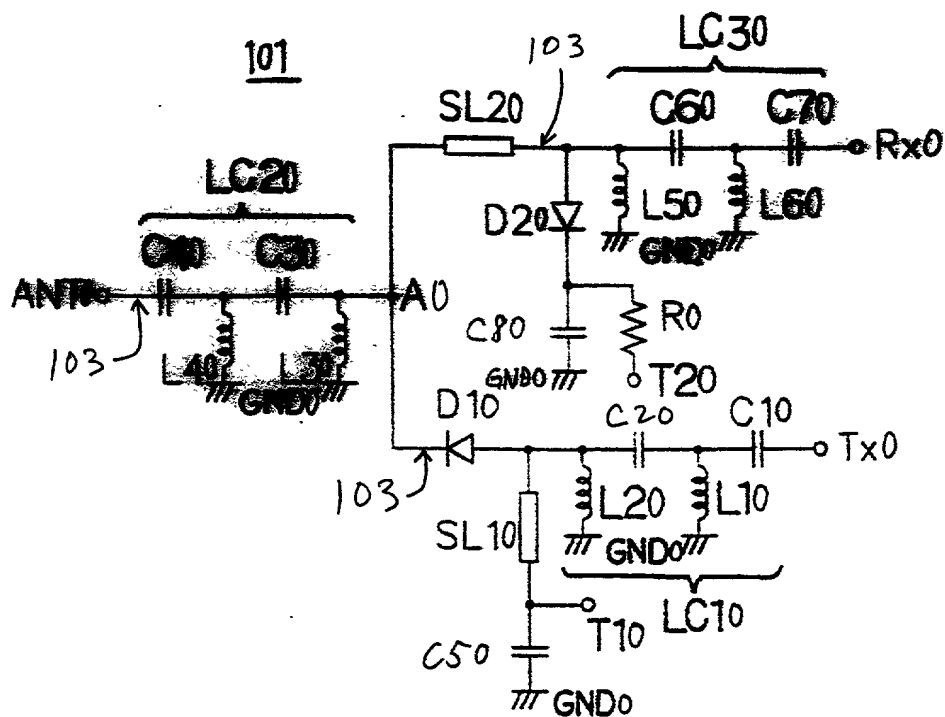
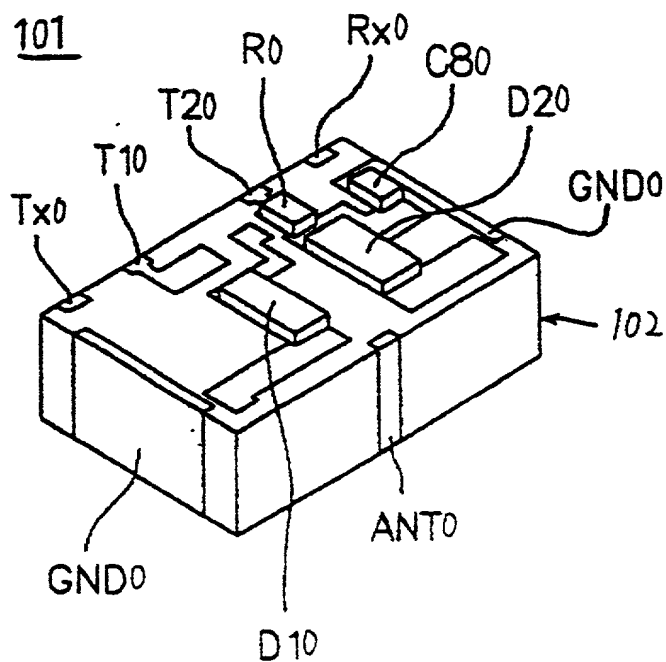


Fig. 5



UNITED STATES OF AMERICA COMBINED DECLARATION AND POWER OF ATTORNEY FOR PATENT APPLICATION		USPTO FILE NO. P/1071-1024	
<p>As a below named inventor, I hereby declare that: my residence, post office address and citizenship are as stated below next to my name; that I verify believe that I am the original, first and sole inventor (if only one name is listed below) or a joint inventor (if plural inventors are named) of the subject matter which is claimed and for which a patent is sought on the invention entitled:</p> <p>HIGH FREQUENCY SWITCHING COMPONENT</p>			
the specification of which is attached hereto, unless the following box is checked:			
<input type="checkbox"/> was filed on _____ as United States patent Application Number or PCT International patent application number _____ and was amended on _____ (if any).			
I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.			
I acknowledge the duty to disclose all information known to be material to patentability in accordance with Title 37, Code of Federal Regulations, §1.56.			
I hereby claim priority benefits under Title 35, United States Code §119 of any foreign application(s) for patent or inventor's certificate or United States provisional application(s) listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:			
Prior Foreign or Provisional Application(s)			
COUNTRY	APPLICATION NUMBER	DATE OF FILING (day, month, year)	PRIORITY CLAIMED UNDER 35 U.S.C. 119
Japan	11-215012	29 July 1999	YES <input checked="" type="checkbox"/> NO <input type="checkbox"/>
Japan	11-215013	29 July 1999	YES <input checked="" type="checkbox"/> NO <input type="checkbox"/>
			YES <input type="checkbox"/> NO <input type="checkbox"/>
I hereby claim the benefit under Title 35, United States Code, §120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, §112, I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, §1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application.			
UNITED STATES APPLICATION NUMBER	DATE OF FILING (day, month, year)	STATUS (patented, pending, abandoned)	
I hereby appoint customer no. 2352 OSTROLENK, FABER, GERB & SOFFEN, LLP, and the members of the firm, Samuel H. Weiner - Reg. No. 18,510; Jerome M. Berliner - Reg. No. 18,653; Robert C. Faber - Reg. No. 24,322; Edward A. Meilman - Reg. No. 24,735; Stanley H. Lieberstein - Reg. No. 22,400; Steven I. Weisburd - Reg. No. 27,409; Max Moskowitz - Reg. No. 30,576; Stephen A. Soffen - Reg. No. 31,063; James A. Pinder - Reg. No. 30,173; William O. Gray, III - Reg. No. 30,944; Louis C. Dujmich - Reg. No. 30,625 and Douglas A. Miro - Reg. No. 31,643, as attorneys with full power of substitution and revocation to prosecute this application, to transact all business in the Patent & Trademark Office connected therewith and to receive all correspondence.			
SEND CORRESPONDENCE TO: OSTROLENK, FABER, GERB & SOFFEN, LLP DIRECT TELEPHONE CALLS TO: 1180 AVENUE OF THE AMERICAS (212) 382-0700 NEW YORK, NEW YORK 10036-8403 CUSTOMER NO. 2352			
I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.			
FULL NAME OF SOLE OR FIRST INVENTOR Hideki MUTO		INVENTOR'S SIGNATURE Hideki Muto	DATE 8 June 2000
RESIDENCE (City and either State or Foreign Country) Omihachiman-shi, Shiga-ken, Japan		COUNTRY OF CITIZENSHIP Japan	
POST OFFICE ADDRESS c/o Murata Manufacturing Co., Ltd., Dept. A170, 26-10, Tenjin 2-chome, Nagaokakyo-shi, Kyoto-fu 617-8555, Japan			
FULL NAME OF SECOND JOINT INVENTOR (IF ANY) Koji TANAKA		INVENTOR'S SIGNATURE Koji Tanaka	DATE 29 May 2000
RESIDENCE (City and either State or Foreign Country) Yasu-gun, Shiga-ken, Japan		COUNTRY OF CITIZENSHIP Japan	
POST OFFICE ADDRESS c/o Murata Manufacturing Co., Ltd., Dept. A170, 26-10, Tenjin 2-chome, Nagaokakyo-shi, Kyoto-fu 617-8555, Japan			
FULL NAME OF THIRD JOINT INVENTOR (IF ANY) Koji FURUTANI		INVENTOR'S SIGNATURE Koji Furutani	DATE 29 May 2000
RESIDENCE (City and either State or Foreign Country) Moriyama-shi, Shiga-ken, Japan		COUNTRY OF CITIZENSHIP Japan	
POST OFFICE ADDRESS c/o Murata Manufacturing Co., Ltd., Dept. A170, 26-10, Tenjin 2-chome, Nagaokakyo-shi, Kyoto-fu 617-8555, Japan			

